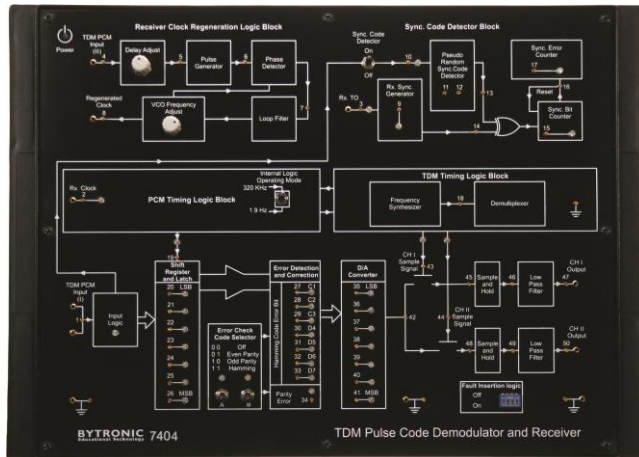


## 7404 – TDM Pulse Code Demodulation Receiver



### Key Features:

- Functional blocks indicated via on board mimics
- Test points
- Input accepts two channel multiplexed data
- On-board demultiplexed PCM receiver
- On-board Low Pass Filter
- Fast and slow modes for real time operation and data flow examination
- On-board PLL for clock regeneration
- On-board sync code detector
- Error check code option
- Odd or even parity – single bit error detection
- Hamming code single bit error detection and correction
- Switched faults allow different error check code options

The 7404 is one of a range of digital communication trainers and provides all necessary inputs and connections for students to study decoding and demultiplexing of data transmitted by model 7403, the pulse code modulation transmitter. On-Board PLL provides regeneration of clock. Synchronization between transmitter and receiver is provided by pseudo random code.

### Experiments

- Study of pulse code modulation
- Study of A/D conversion
- Study of control signals and their timing
- Study of time division multiplexing
- Study of synchronization by pseudo random code
- Study of error check codes
- Three modes of operation to regenerate original signal

### Specification

Input Channels	TDM Serial Input
Demodulation	Pulse Code Demodulation
Clock	Regeneration by PLL
Operating Speed	Fast-320 KHz/channel; Slow 1.9MHz/channel
Error Detection	Off-Odd-Even Parity Hamming Code
Error Correction	Hamming code
Test Points	50
Power	110-220V $\pm$ 10% 50/60Hz
Dimensions (mm)	W326, H52, D252
Weight	1.3Kg (approx.)

## Ordering Information

**Model Number:**

**7404**

*Consists of:*

TDM Pulse Code Modulation Receiver Trainer

Manuals

Power Cord

Set of patch cables

### *Notes.*

*1. Specification is subject to change without notice.*

*2. All dimensions are in mm unless otherwise stated*

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