

CPLD/FPGA – Digital Logic Circuit Design Trainer



Key Features:

- Supports ALTERA and XILINX chips
- Connection to PC through USB
- D/A and A/D Converters

In today's environment CPLD (Complex Programmable Logic Devices) and FPGA (Field Programmable Gate Arrays) are the first-choice components for the design of applications in the communication, industrial automation, image processing and extensive engine control areas. To allow users to have an exceptional experimental platform, the CPLD trainer has support for Altera or XILINX chips.

Specification

Supported chips:	ALTERA: FLEX10K30ATC144 (TQFP-144) / 5CEFA2 F23C8N (BGA-484) XILINX: XC2S50 (PQFP-208)
Signal generation unit:	Programmable frequency generator Standard frequency 1K/10K/100K/1M/10MHz
Logic input switch:	8 x 1 Logic input original press point with light. 8 x 2 Logic input Dip switch 4 Impulse press button generator (2 positive pulse; 2 negative pulse) 3 x 4 Array keyboard.
Output unit:	1 x 8 x 8 dual colour point array LED display 1 x LCD 16 x 2 display 1 x 6 digits 7 segment display 1 x 3 x 4 LED output. 1 x Buzzer
Linear unit:	2 x 8-bit D/A converter 1 x 8-bit A/D converter
MPU unit:	8051 and CPLD/FPGA match circuit test
Power requirements	100-240V AC
Frequency Range	50/60Hz
Communications	USB interface
Dimensions	320mm x 226mm x 30/85mm
Weight	3kg
Operating System	Windows 98/2000/XP

Ordering Information

Model Number:

CPLD

Consists of:

CPLD Board

CD

USB Cable

Power Cord

Notes.

1. *Specification is subject to change without notice.*

2. *All dimensions are in mm unless otherwise stated*

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